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SARDAR PATEL UNIVERSITY

M.Sc. (Electronics) First Semester Examination

November 2012

PS01CELE02 : Applications of ICs and Fuzzy Electronics

Monday, December 03, 2012

Time : 10.30 a.m. to 1.30 p.m.

Total Marks : 70

Q. 1	<p>Give the correct (nearest) answer (statement) to the following Multiple Choice Questions (Statements).</p> <ol style="list-style-type: none"> For the Bandpass Filter, Critical frequency f_0 is given by <ol style="list-style-type: none"> $f_0 = Q \times BW$ $f_0 = BW \cdot Q$ $f_0 = BW/Q$ None of the above The Elliptical filters are specified in terms of <ol style="list-style-type: none"> f_c, n and A_{max} f_c and n f_c, n, A_{max} and A_{min} In the edge-triggered Phase Detector, the dc output voltage is linear over <ol style="list-style-type: none"> π radians 2π radians $2\pi/3$ radians Which of the following pSpice commands gives the quiescent point in the dc analysis of a circuit? <ol style="list-style-type: none"> .DC .OP .TF .END One of the following operations makes a set less Fuzzy <ol style="list-style-type: none"> Concentration Union Dilation Algebraic Sum The bounded difference of two fuzzy sets A and B is denoted by $C = A \odot B$, where, <ol style="list-style-type: none"> $\mu_C(x) = \min(1, (\mu_A(x) - \mu_B(x)))$ $\mu_C(x) = \min(1, (\mu_A(x) + \mu_B(x)))$ $\mu_C(x) = \mu_A(x) + \mu_B(x) - \mu_A(x) \cdot \mu_B(x)$ 	<p>8x1 = [8]</p> <p>P.T.O.</p>
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	<p>7) The Measure of Fuzziness $E(A)$ is given by</p> <p>a) $E(A) = M(A\bar{A}) / M(A\bar{V})$</p> <p>b) $E(A) = M(A\bar{V}) / M(A\bar{A})$</p> <p>c) None of the above</p> <p>8) Which of the following Fuzzy Learning Rule Forgetting is not incorporated ?</p> <p>a) Hebbian</p> <p>b) Grossberg</p> <p>c) None of the above</p>	
Q. 2	<p>Give short answers to the following: (any seven)</p> <ol style="list-style-type: none"> 1) Mention the Drawbacks of Active Filters over Passive Filters. 2) Describe the working of a First Order Active Low Pass Filter. Explain how the Roll Off Rate is 20 db/decade. 3) Discuss various types of approaches for obtaining Higher Order filters with their merits and demerits . 4) For PLL configured using NE 565, using $R_1 = 30K\Omega$, $C_1 = 0.01\mu F$, $C_2 = 10\mu F$, and the Supply Voltage is $\pm 10V$. Calculate the Free Running Frequency. 5) In a DC circuit, the DC voltage is swept from 5V, 20V, and 30V, connected between nodes 1 and 0, whereas the current source connected between nodes 0 and 4 with current swept from 50 mA, to 100mA to 150mA. Write the pSpice command for the same. 6) Draw the Fuzzy Inverter Circuit for the Current Mode Fuzzy Logic Circuits. 7) Describe the Kohonen Learning Rule for the Neural Network. 8) Draw the truth table of Fuzzy AND operation. 9) For a Fuzzy Set $A = [0.3 \ 0.8]$, find out the measure of Fuzziness. 	7x2 = [14]
Q.3 (a)	Draw the circuit diagram of Multiple Feedback Bandpass Filter and deduce the equation for its Transfer Function.	[6]
(b)	Design a Sallen Key Unity Gain (SKUG) Second Order Low Pass Filter and deduce the equation for its Transfer Function.	[6]
(b)	<p style="text-align: center;">OR</p> <p>Design a Low Pass Filter for the given specifications : $f_c = 3 \text{ kHz}$, $K = 1.5$, $R_a = 20 \text{ kohm}$, $C = 10\text{nF}$, Roll Off rate = 40 dB/decade</p>	[6]
Q.4 (a)	What Is a Generalized Impedance Converter (GIC)? Draw the circuit diagram of Grounded Inductor using GIC and deduce the equation for such inductor.	[6]

(b)	With the help of the block diagram and necessary circuit diagram, describe the application of Phase Locked Loop as Frequency Multiplier.	[6]
(b)	<p>OR</p> <p>Using the SKUG configuration, design a Third Order High Pass Butterworth Filter for critical frequency of 2KHz.</p> <p>From the Filter Standard table for Low Pass Butterworth Filter, for $n(\text{order}) = 3$, $f_{01} = 1$, $Q_1 = 1.000$ and $f_{02} = 1$.</p>	[6]
Q.5 (a)	<div data-bbox="288 510 1134 813" data-label="Diagram"> </div> <p>Simulate the DC Circuit shown in the Figure using the pSpice to calculate all Node Voltages and Currents.</p>	[6]
(b)	Giving the importance of Current Mode Circuits, describe the Fuzzy Logic Circuits for Bounded Difference and AND operation.	[6]
(b)	<p>OR</p> <p>What Is Phase Locked Loop (PLL)? Draw the Block Diagram of PLL and describe the function of each block.</p>	[6]
Q.6 (a)	What is Fuzzification? Describe the process of Grade Fuzzification giving suitable example.	[6]
(b)	<p>For Fuzzy Sets given as $\tilde{A} = 0.4/1 + 0.3/2 + 0.2/3$ and $\tilde{B} = 0.31 + 0.2/2$, perform the following operations:</p> <p>(i) Union (ii) Intersection (iii) Bounded Difference (iv) Algebraic Product (v) Concentration of B (vi) Absolute Complement of A.</p> <p>OR</p>	[6]
(b)	What is Neural Network ? Explain the working of Biological Neuron and how a mathematical model can be used to mimic its function.	[6]
